

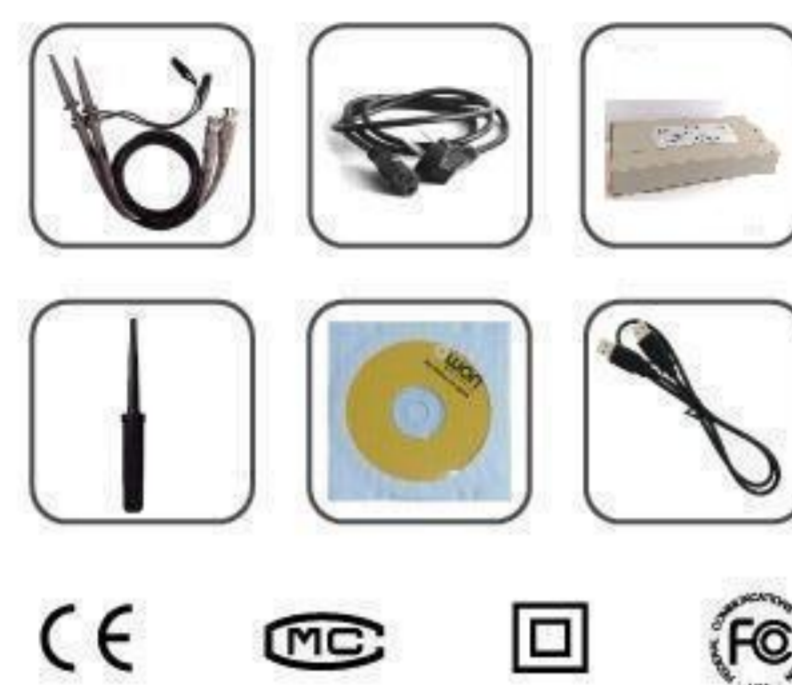
Order code	Manufacturer code	Description
85-2780	n/a	100MHZ 2 CHL SCOPE WITH LOGIC ANALYZER R
85-2797	n/a	25MHZ 2 CHL SCOPE WITH LOGIC ANALYZER RE

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The enclosed information is believed to be correct, Information may change without notice due to product improvement. Users should ensure that the product is suitable for their use. E. & O. E.	Revision A 20/02/2007



MSO series

1. SUPPORT LOGIC ANALYZER
2. 7.8 INCH COLOR STN PANEL
3. 32-16 OPTIONAL LOGICAL INPUT CHANNEL
4. USB CONNECTION



PERFORMANCE CHARACTERISTICS

Bandwidth

MSO5022: 25MHz MSO710 2 100MHz

Channels

2 + External+ Logical Analyzer

Acquisition

Mode: Normal, Peak detect, Averaging
 Sample rate (real time): MSO5022: 100MS/s MSO710 2 :500MS/s

Input

Input coupling: DC, AC
 Input impedance: 1 M \pm 2% in parallel with 20pF \pm 3pF
 Probe attenuation factors: 1X, 10X, 100X, 1000X
 Max. input voltage: 300V(DC-AC Peak, 1M Input impedance)(10:1 Probe attenuation)

Horizontal system

Sampling range: MSO5022: 10S/s ~ 100MS/s MSO710 2: 10S/s ~1000MS/s
 Record length: Max. 5K points
 Time base range (S/div): MSO5022: 5ns/div ~ 5s/div (step as 1~2.5~5)
 MSO710 2: 5ns/div ~ 5s/div (step as 1~2~5)
 Time base accuracy: 100ppm

Vertical system

Vertical resolution(A/D): 8 bits
 Vertical sensitivity: 5mV/div ~ 5V/div (Input to BNC)
 Position range: MSO5022: \pm 10div(5mV/div ~ 5V/div)
 MSO 2: \pm 50V(500mV~5V) \pm 2V(5mV~200mV)
 Simulate Bandwidth: MSO5022: 25M MSO710 2: 100 M
 Single bandwidth: Full bandwidth
 LF Respond (AC, -3dB): \geq 5Hz (to BNC)
 Rising time (typical on BNC): MSO5022: \leq 14 ns MSO710 2: \leq 5.8 ns
 DC Gain accuracy: \pm 5%

PERFORMANCE CHARACTERISTICS

Trigger

Trigger mode: Edge: Up, Down Video: line in-phase, field in-phase
 Trigger sensitivity: DC coupling: CH1 and CH2: 1div (DC~Full bandwidth)
 EXT: MSO5022: 100mV(DC~25M)
 MSO710 2: 100mV(DC~20M)
 EXT/5: MSO5022: 500mV(DC~25M)
 MSO710 2: 500mV(DC~20M)
 AC coupling: 1div(50Hz-Full bandwidth)
 Internal: \pm 6 divisions from center screen
 EXT: \pm 600mV EXT/5: \pm 3V
 Internal: \pm 0.3 divisions
 EXT: \pm (40mV +6% setting value) EXT/5: \pm (200mV +6% setting value)
 Trigger displacement: Front trigger 655 divisions, back trigger 4divisions
 Setting level to 50% (typical)
 Trigger Sensitivity (Video trigger, typical): When Input signal frequency \geq 50Hz
 Internal: 2 divisions between peaks
 EXT: 400mV EXT/5: 2V
 Support NTSC, PAL and SECAM
 Line/field frequency(Video): Support NTSC, PAL and SECAM

Measurement system

Cursors measurement: Δ V, Δ T
 Automatic measurement: PK-PK, Averaging, RMS, Frequency, Circle
 Waveform math: +, -, INVERTED
 Waveform storage: 4 waveforms, 4 settings
 Lissajou's figure: Available

Probe compensation output

Output voltage (typical): Approx 5V, PK-PK \geq 1M loading
 Frequency (typical): 1KHz square wave

Power supply

Voltage: 100 ~ 240 VAC, 50Hz, CAT II
 Power consumption: \leq 15W
 Fuse: 1A, T class, 250V

specification

Size: 350mmx157mmx120mm
 Weight: 1kg

LOGICAL ANALYZER CHARACTERISTICS

Max sampling rate	100M /400MS/s	Trigger position setting	Pre-trigger, mid-trigger, re-trigger
Input Channel	16	Trigger Mode	Bus trigger, state trigger, data alignment trigger, data width trigger
Max Storage	4M/Channel	Data waveforms call-back	Support
Measurement bandwidth	33MHz/ 66Mhz	Data search	Support
Input impedance	1M \pm 2%	Data System	binary system, decimal system, hex
Voltage	0~4V		
Input Signal Range	0~5V		

ACCESSORIES: 1 pair of 1:1(10:1) passive probe, USB cable (RS232 optional), Power cable
 Instruction manual, Driver CD, Adjustment probe compensation pen