

DIL32/TQFP32-1 ZIF

(ord.no. 70-0135)

- universal adapter, assigned for devices in QFP32/LQFP32/TQFP32/VQFP32 package (for example Silicon Laboratories C8051 series, STM ST7xxx series, Atmel ATmega, ATTiny series, Motorola/ Freescale HC08 series, Renesas R8C/Tiny series)
- operation (mechanical) life of ZIF socket - 10.000 actuations
- made in Slovakia

Ord. no	70-0135
Socket	ZIF QFP32, open top type
Bottom	2 rows, 2x 16 pins, square, 0.6x0.6mm, rows spacing 600mil
Class	Universal
Subclass	DIL/QFP



Adapter manual

- Insert adapter to the device programmer ZIF socket according to the picture placed near of it. If you have some doubts about orientation of this adapter in device programmer ZIF socket, it is valid general rule, the orientation of the text of title is the same as the text on the top of the device programmer.
- Push the top of adapter ZIF socket to open it. Insert the device into the adapter ZIF socket. The right position of the programmed device in adapter ZIF socket is shown at picture near (mainly left above) the adapter ZIF socket. On this picture reference corner (e.g. position of pin 1) of device is indicated by dot, by number 1, by bezel or by any combination of them. Then release adapter ZIF socket.
- Visually check interconnection between device and adapter ZIF socket. If everything looks OK, the device is ready for programming.
- Be careful, because the incorrect insertion of adapter to the device programmer ZIF socket or device to the adapter ZIF socket can damage the programmed device.
- To take out the device push the top of adapter ZIF socket and remove device from it.
- When you finish work with adapter, remove it from the device programmer ZIF socket.
- Do not directly touch the pins of the adapter and adapter ZIF socket, because dirt may cause errors during programming of device.
- For handling with the device we recommended to use a vacuum pick up tool.
- If programming yields isn't 100% when using programming adapter or if some unreliability appears, try to connect one 22nF-100nF multilayer capacitor (that meet the EIA X7R or Z5U specification) between leads of adapter connected to pins VCC and GND pins of programmed chip.

Software note

- If software version, you're using currently, doesn't contain support for this programming adapter, please download the latest version of software - Regular or OnDemand - from our web site.

Accepted package(s)

