

SIL Reed Relays

Order code: 60-0670

1 Features:

- 1-1 Epoxy molded single in line structure
- 1-2 Higher mounting density than DIP type
- 1-3 Completely washable
- 1-4 Miniature cost effective switching solution
- 1-5 Is a UL recognised component (E155181)



2-1 ELECTRICAL

2-1-1 Contact form 1A

2-1-2 Contact rating

 (a) Maximum switching power
 10VA

 (b) Maximum switching voltage
 100VDC

 (c) Maximum switching current
 0.5ADC

 (d) Maximum carry current
 1.0ADC

 2-1-3 Contact resistance
 150mΩ max.

 2-1-4 Operate time
 1.0ms max.

(including bounce time)

2-1-5 Release time

2-1-6 Dielectric strength

 $\begin{array}{c} \text{(a) Coil to contact} & 1400\text{VDC} \\ \text{(b) Between contacts} & 250\text{VDC} \\ \text{2-1-7 Insulation resistance} & 10^{10}\Omega \text{ min.} \\ \text{2-1-8 Shock resistance} & 30G \text{ min.} \\ \end{array}$

2-1-9 Coil rating:

Nominal voltage (VDC)	Operate voltage (V) max.	Pull in voltage (V)	Drop out voltage (V)	Coil resistance (Ω)	Nominal input power (mW)
5	16	3.75	0.8	500 ±10%	50
12	20	9.00	1.0	1000 ±1%	144
24	32	18.00	2.0	2150 ±10%	268

0.5ms max.

2-2 MECHANICAL AND ENVIRONMENTAL

2-2-1 Temperature range:

Operating temperature -40°C to $+85^{\circ}\text{C}$ Storage temperature -55°C to $+125^{\circ}\text{C}$ 2-2-2 Life expectancy 100×10^{6}

(Def 4 OVDO 4 Ove 4)

(Ref 10VDC, 10mA) 2-2-3 Solderability

After flux 230 ±5°C for 5 ±0.5 seconds 95% coverage

2-2-4 Reflow soldering heat for SMT type Reflow zone 230 ±5°C for 30 seconds

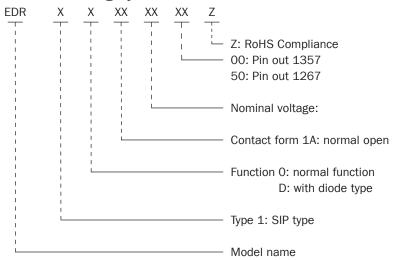
(reference only)



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3 Part numbering system:



4 Packing:

4-1 Packing method:

Relays are packed into IC tubes as an inner packing unit, then IC tubes are packed into boxes.

4-2 Information on the label (stuck to each tray)

(1) DESCRIPTION.

(6) ECE MARK.

(2) LOT NO.

(7) QC STAMP.

(3) REPROCESS NO.

(8) DATE.

(4) QUANTITY.

(9) MANUFACTURER'S NAME.

(5) TESTER NO.

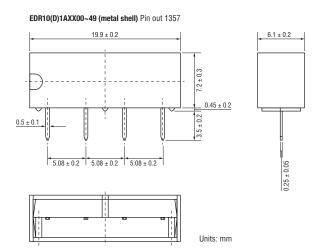
5 Drawing:

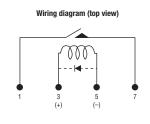
See drawing on page 3 for: Reed relay dimensions; Wiring diagram and PCB layout diagram.

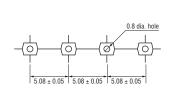


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PCB layout (top view)

